

Connectors



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G.1 CPU/Memory Board

Figure G-1 shows the locations of the centerplane and CPU module connectors.

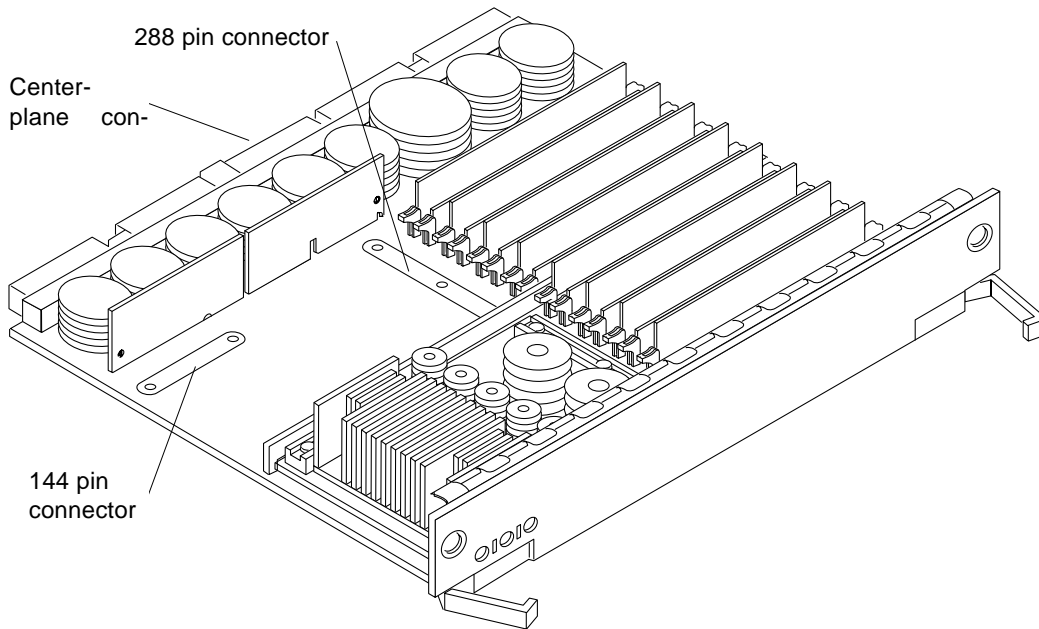


Figure G-1 CPU/Memory Board Connector Locations

G.1.1 Centerplane Connector

Note – Information for the CPU/Memory board centerplane connector also applies to the I/O board centerplane connector.

The CPU/Memory board centerplane connector is mounted on the rear of the board, opposite the side of the board with the LED display and extraction levers. See Figure G-2. The connector is comprised of modular sections, each with its own set of receptacle pins. See Figure G-3.

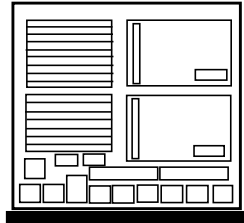


Figure G-2 CPU/Memory Board Centerplane Connector Location

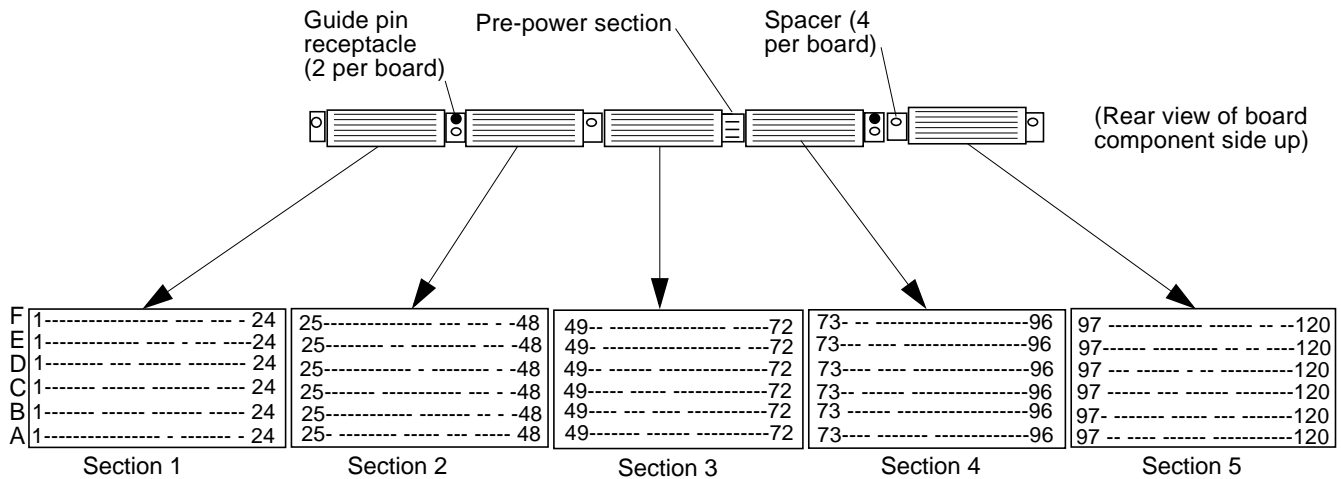


Figure G-3 Centerplane Connector Detail

A three-bladed power section results in 12 pins on the board. There are six, 120 pin rows, labeled A through F, spread across the remaining five sections; each section has 24 pins of each 120 pin row.

G.1.2 CPU Module Connectors

There are two connectors for each CPU module, one with 144 pins and the other with 288 pins. See Figure G-4 through Figure G-7.

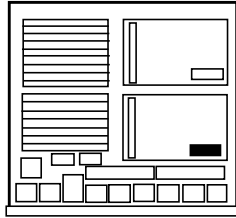


Figure G-4 CPU Module 0 Connector (144 Pin) Location

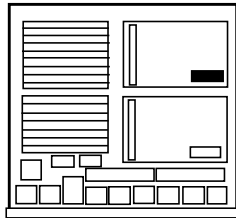


Figure G-5 CPU Module 1 Connector (144 Pin) Location

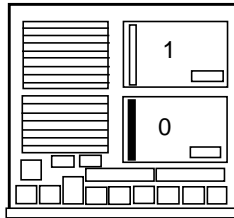


Figure G-6 CPU Module 0 Connector (288 Pin) Location

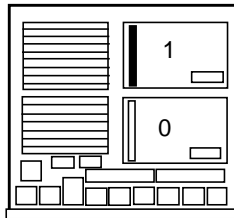


Figure G-7 CPU Module 1 Connector (288 Pin) Location

G.2 I/O Boards

Figure G-8 and Figure G-9 show the locations of the connectors on the SBus I/O and Graphics I/O boards.

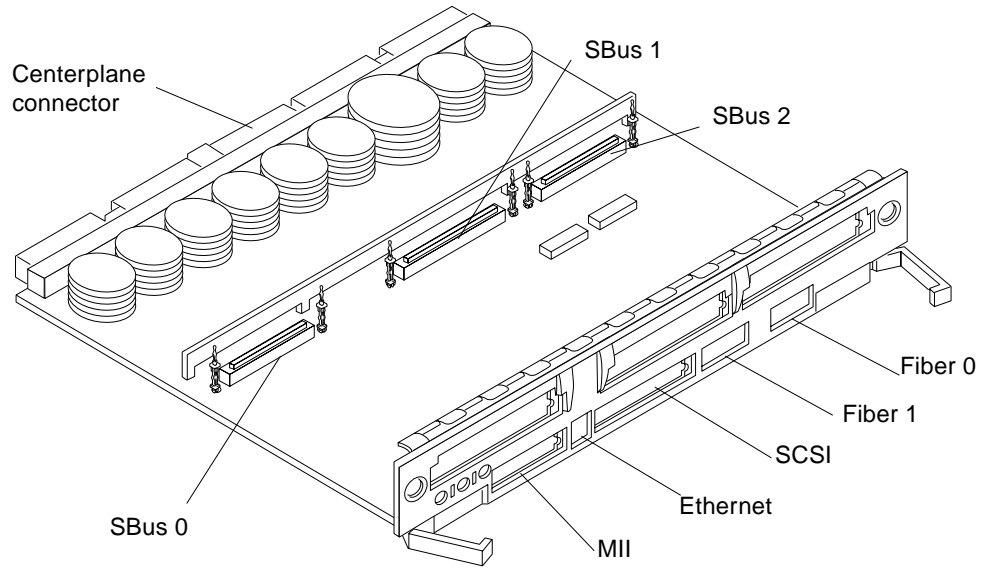


Figure G-8 SBus I/O Board Connector Locations

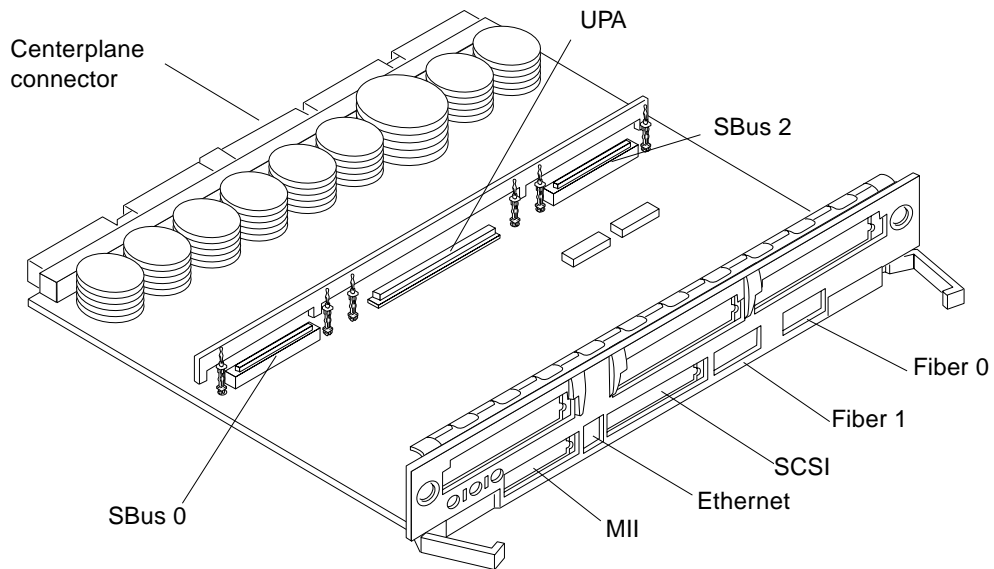


Figure G-9 Graphics I/O Board Connector Locations

G.2.1 Centerplane Connector

See Section G.1.1, “Centerplane Connector,” on page G-2. Information for the CPU/Memory board centerplane and the I/O boards centerplane is the same.

G.2.2 SBus Connectors

The SBus I/O board has three SBus connectors (SBus 0, 1, and 2). The Graphics I/O board has two (SBus 0 and 2). See Figure G-10, Figure G-11, and Figure G-12.

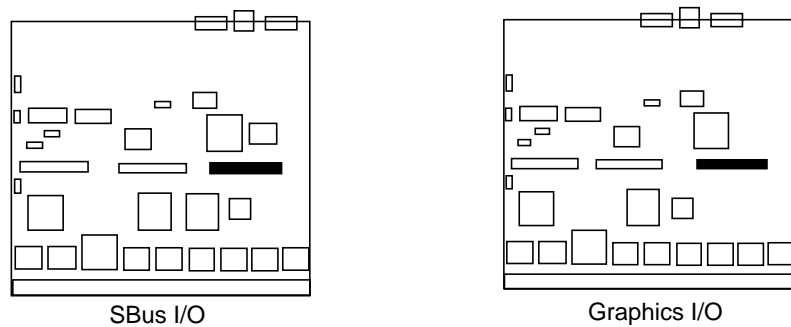


Figure G-10 I/O Board SBus 0 Connector Locations

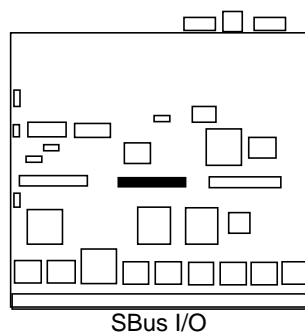


Figure G-11 I/O Board SBus 1 Connector Location

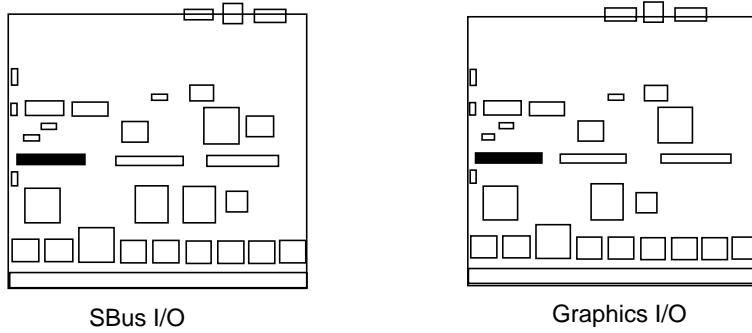


Figure G-12 I/O Board SBus 2 Connector Locations

G.2.3 UPA Connector Location

The Graphics I/O board has one UPA connector.

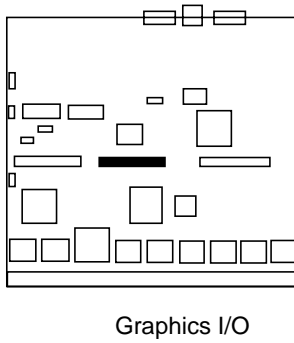


Figure G-13 Graphics I/O Board UPA Connector Location

G.2.4 Ethernet Connector (TPE)

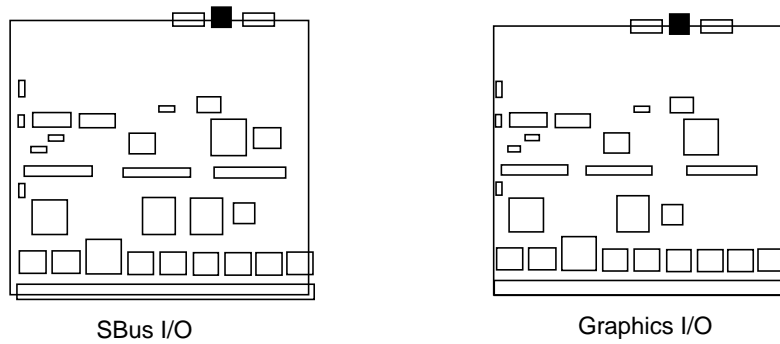


Figure G-14 I/O Board Ethernet Connector Locations

G.2.5 MII Connector

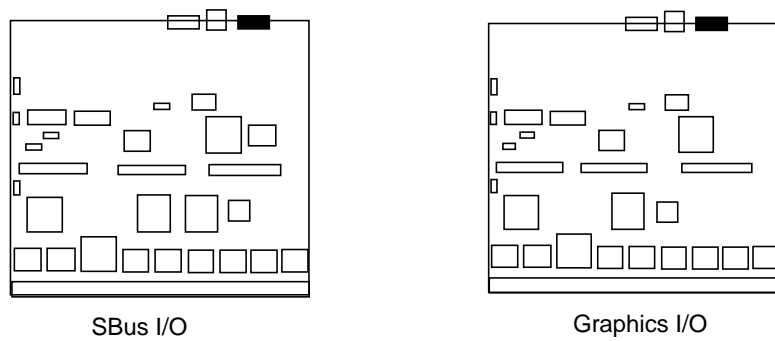


Figure G-15 I/O Board MII Connector Locations

G.2.6 Fiber Interface Connectors

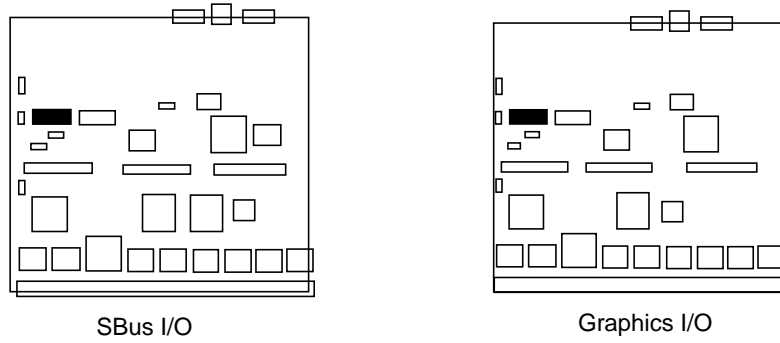


Figure G-16 I/O Board Fiber 0 Interface Connector Locations

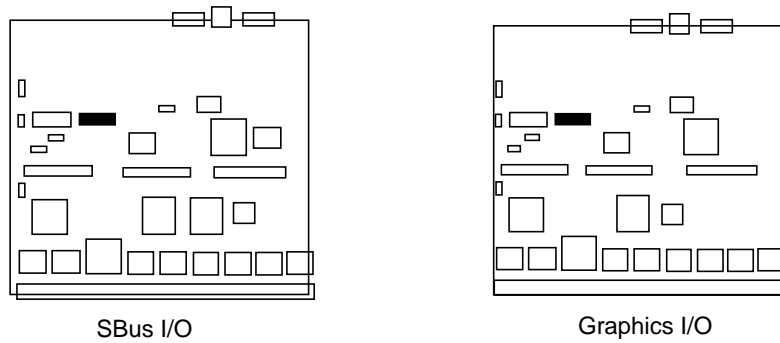


Figure G-17 I/O Board Fiber 1 Interface Connector Locations

G.2.6.1 SCSI Connector

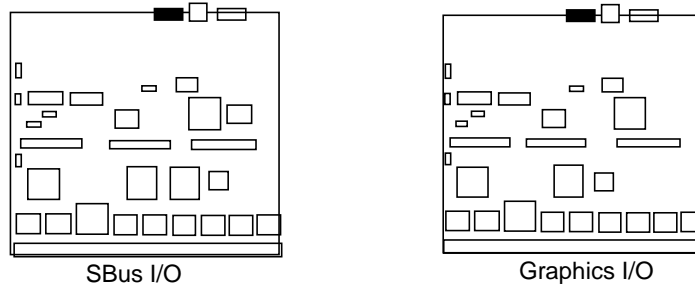


Figure G-18 I/O Board SCSI Connector Locations

G.3 Clock Board

Figure G-19 shows the clock board connector locations.

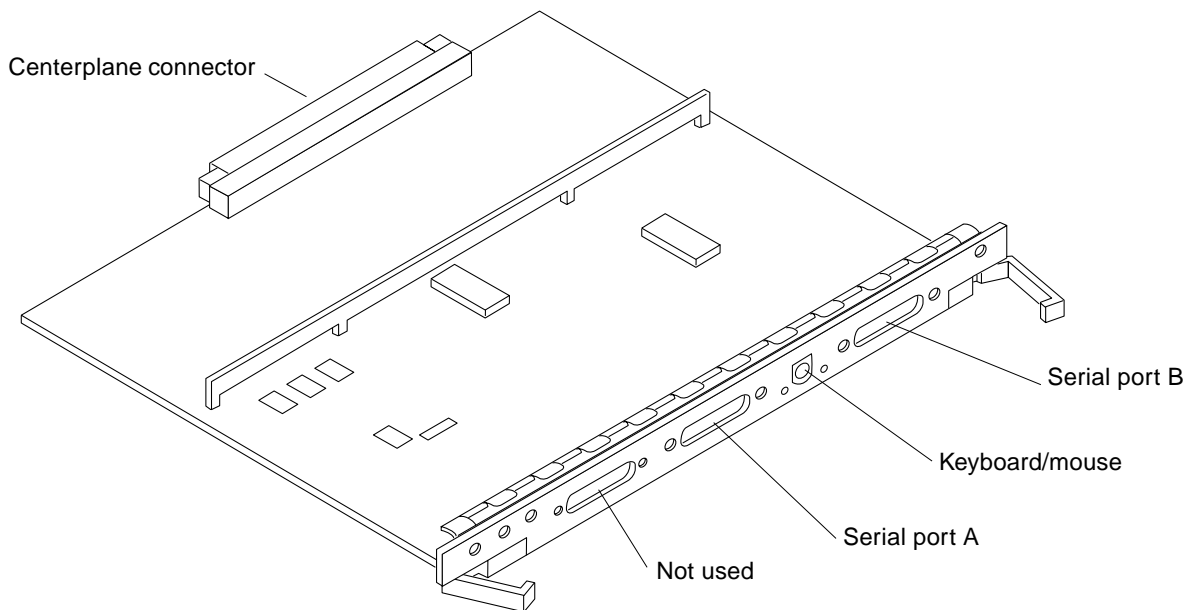


Figure G-19 Clock Board Connector Locations

G.3.1 Centerplane Connector

The clock board centerplane connector interfaces the board to the system. The connector is divided into six rows of pins labeled A through F; each row has 72 pins.



Figure G-20 Clock Board Centerplane Connector Location

G.3.2 Serial Port Connectors

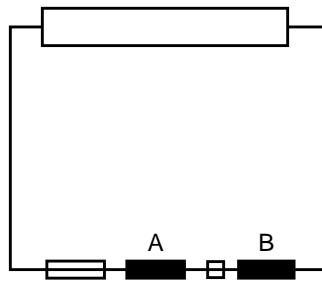


Figure G-21 Clock Board Serial Port Connectors Locations

		1			1
	14	2	TXDA	DBB	14
DBA	15	3	RXDA		2
	16	4	RTSA		3
DDA	17	5	CTSA	DDB	16
	18	6	DSRA		4
	19	7	GND		5
DTRA	20	8	DCDA	DTRB	17
	21	9			6
	22	10			7
	23	11			8
DAA	24	12		DAB	18
	25	13			9
					10
					11
					12
					13

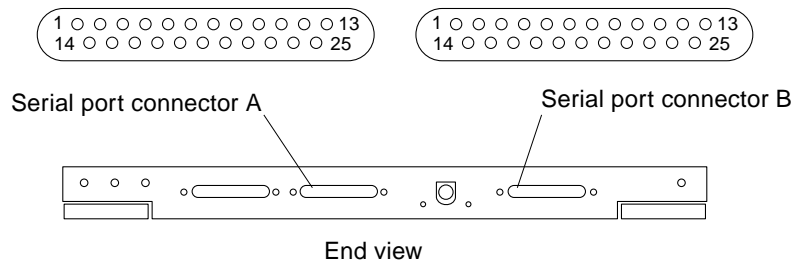


Figure G-22 Serial Port A and B Connector Pinouts

G.3.3 Keyboard and Mouse Connector

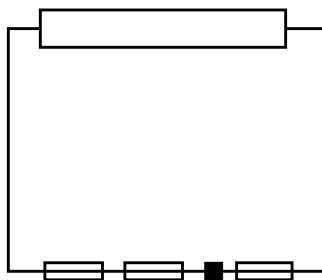


Figure G-23 Clock Board Keyboard/Mouse Connector Location

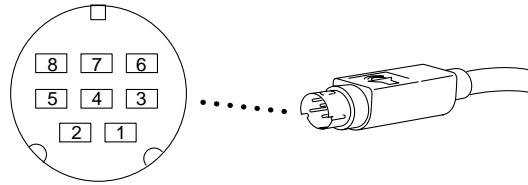


Figure G-24 Keyboard and Mouse Connector Pinouts